

B' if said memory access testing means determine a memory access is not required, then supplying of control signals to said instruction fetching means to fetch the next instruction group during the execution of the current said instruction group.

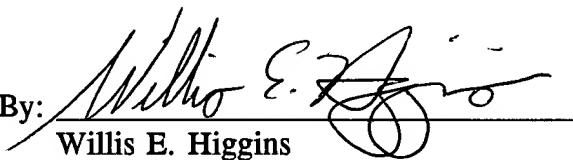
REMARKS

Accordingly, in view of the above remarks, it is submitted that this application is now ready for allowance. Early notice to this effect is solicited.

If in the opinion of the Examiner, a telephone conference would expedite the prosecution of the subject application, the Examiner is invited to call the undersigned at (415) 843-5000.

Respectfully submitted,

COOLEY GODWARD CASTRO
HUDDLESON & TATUM

By: 
Willis E. Higgins
Reg. No. 23,025

Cooley Godward Castro
Huddleson & Tatum
Five Palo Alto Square
3000 El Camino Real
Palo Alto, CA 94306-2155
(415) 843-5000